

FORM PTO 1390 (REV 5-99)		US DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NUMBER 2000_0545A
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. §371			U.S. APPLICATION NO. (if known) 097530396
International Application No. PCT/JP99/04510	International Filing Date August 23, 1999	Priority Date Claimed August 31, 1998	
Title of Invention STORAGE-TYPE DATA RECEIVER			
Applicant(s) For DO/EO/US Hidemi HENMI			
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:			
<p>1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. §371.</p> <p>2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. §371.</p> <p>3. <input checked="" type="checkbox"/> This express request to begin national examination procedures (35 U.S.C. §371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. §371(b) and PCT Articles 22 and 39(1).</p> <p>4. <input type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.</p> <p>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. §371(c)(2))</p> <p style="margin-left: 20px;">a. <input type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau).</p> <p style="margin-left: 20px;">b. <input checked="" type="checkbox"/> has been transmitted by the International Bureau. ATTACHMENT A</p> <p style="margin-left: 20px;">c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US)</p> <p>6. <input checked="" type="checkbox"/> A translation of the International Application into English (35 U.S.C. §371(c)(2)). ATTACHMENT B</p> <p>7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. §371(c)(3)).</p> <p style="margin-left: 20px;">a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau).</p> <p style="margin-left: 20px;">b. <input type="checkbox"/> have been transmitted by the International Bureau.</p> <p style="margin-left: 20px;">c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</p> <p style="margin-left: 20px;">d. <input type="checkbox"/> have not been made and will not be made.</p> <p>8. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. §371(c)(4)). ATTACHMENT C</p> <p>9. <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. §371(c)(5)).</p> <p><b>Items 10. to 13. below concern other document(s) or information included:</b></p> <p>10. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</p> <p>11. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included. ATTACHMENT D</p> <p>12. <input checked="" type="checkbox"/> A FIRST preliminary amendment. ATTACHMENT E</p> <p style="margin-left: 20px;"><input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment.</p> <p>13. <input checked="" type="checkbox"/> Other items or information:</p> <ul style="list-style-type: none"> <li>- Notification Concerning Submission or Transmittal of Priority Document - ATTACHMENT F</li> <li>- International Search Report - ATTACHMENT G</li> </ul>			

THE COMMISSIONER IS AUTHORIZED  
TO CHARGE ANY DEFICIENCY IN THE  
FEES FOR THIS PAPER TO DEPOSIT  
ACCOUNT NO. 23-0975

U.S. APPLICATION NO. <b>09/530396</b> <small>(If known, 37 CFR 1.53)</small>		INTERNATIONAL APPLICATION NO. PCT/JP99/04510		ATTORNEY'S DOCKET NO. 2000 0545A	
17. [X] The following fees are submitted  <b>BASIC NATIONAL FEE (37 CFR 1.492(a)(1)-(5)):</b> [X] Search Report has been prepared by the EPO or JPO..... \$840.00 [ ] Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO..... \$970.00  <b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				CALCULATIONS	PTO USE ONLY
Surcharge of \$130.00 for furnishing the oath or declaration later than [ ] 20 [ ] 30 months from the earliest claimed priority date (37 CFR 1.492(e)).				\$	
Claims	Number Filed	Number Extra	Rate		
Total Claims	15 - 20 =	-0-	X \$18.00	\$	
Independent Claims	2 - 3 =	-0-	X \$78.00	\$	
Multiple dependent claim(s) (if applicable)			+ \$260.00	\$	
<b>TOTAL OF ABOVE CALCULATIONS =</b>				\$840.00	
Reduction by 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28)				\$	
<b>SUBTOTAL =</b>				\$840.00	
Processing fee of \$130.00 for furnishing the English translation later than [ ] 20 [ ] 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				+	\$
<b>TOTAL NATIONAL FEE =</b>				\$840.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40 per property				+	\$ 40.00
<b>TOTAL FEES ENCLOSED =</b>				\$880.00	
				Amount to be refunded	\$
				Amount to be charged	\$
[X] A check in the amount of <u>\$880.00</u> to cover the above fees is enclosed. A duplicate copy of this form is enclosed. [ ] Please charge my Deposit Account No. 23-0975 in the amount of \$_____ to cover the above fees. A duplicate copy of this sheet is enclosed. c. [X] The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>23-0975</u> .					
<b>NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.</b>					
SEND ALL CORRESPONDENCE TO:  WENDEROTH, LIND & PONACK, L.L.P. 2033 K Street, N.W., Ste. 800 Washington, D.C. 20006			<div style="text-align: center;">          SIGNATURE       </div> <div style="text-align: center;"> <u>Charles R. Watts</u>          NAME       </div> <div style="text-align: center;"> <u>33,142</u>          REGISTRATION NUMBER       </div>		
April 28, 2000					

[CHECK NO. 37822]  
 [2000\_0545A]

526 Rec'd PCT/PTO

28 APR 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :  
Hidemi HENMI : Attn: BOX PCT  
Serial No. NEW : Docket No. 2000\_0545A  
Filed April 28, 2000 :

STORAGE-TYPE DATA RECEIVER

[Corresponding to PCT/JP99/04510  
Filed August 23, 1999]

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents,  
Washington, DC 20231

Sir:

Please amend the above-identified application as follows.

In the Specification and Abstract:

- Page 13, line 4, change "storage controller 13" to --data storage 17--;  
line 18, change "the decoder 5" to --the decoder 3--;  
Page 15, line 22, change "the update time T (inu) to --the next-update time T (inu)--;  
Page 16, line 2, change "this step" to --step 40 and step 50--;  
Page 18, line 1, change "FIG. 2" to --FIG. 4--;  
Page 19, line 21, change "a tuner control part 12" to --the tuner controller 23--;  
Page 21, line 5, change "Arp3" to --Arp2--;  
line 17, change "before" to --between--;  
Page 24, line 7, change "Arp1" to --Arp3--;  
Page 25, line 8, change "S4" to --S4R--;  
line 19, after "S4R" insert --based on--;

ATTACHMENT E

Page 26, line 19, change "De" to --Dd--;  
line 25, change "De" to --Dd--.

**In the Claims:**

Kindly amend claims 1, 5, 7 and 8 as follows.

Claim 1, line 6, after "next-update" insert --time--;

Claim 5, line 3, change "said said data update" to --said data update time--

Claim 7, line 3, change "said said data update" to --said data update time--;

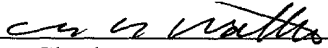
Claim 8, line 3, after "stored" insert --wherein--.

**REMARKS**

Entry of this Preliminary Amendment prior to initial examination of the application is respectfully requested.

Respectfully submitted,

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## SPECIFICATION

### STORAGE-TYPE DATA RECEIVER

5

#### TECHNICAL FIELD

The present invention relates to a storage-type data receiver used for broadcast-type data distribution service such as text broadcasting or data broadcasting, and communication-type data distribution service such as the Internet, and more specifically, relates to a recording medium on which a data reception processing program is recorded for realizing, exemplarily on a computer, a data reception method and a stored data update method carried out in the storage-type data receiver.

15

#### BACKGROUND ART

Recently, broadcast-type data distribution service such as text broadcasting or data broadcasting for distributing data by means of broadcast signals has started. Communication-type data distribution service for distributing data through a network such as the Internet has been also introduced. Accordingly, a variety of data receivers have appeared on the market, which are of a type receiving data distributed through such data distribution services and then storing the received data for later use.

In the broadcast-type data distribution service, for example, information including program title, broadcast time,

broadcasting station, and the cast is inserted between conventional television broadcast waves in analog (VBI in Vertical Blanking Interval) or directly into satellite broadcast waves in digital as EPG (Electric Program Guide) data.

5           FIG. 10 shows the structure of a conventional storage-type data receiver used for broadcast-type data distribution service. A conventional storage-type data receiver Arc is provided with a tuner 101, a data extractor 103, a data decoder 105, a data storage 107, and a data presenter 109.

10           In the tuner 101, broadcast wave Srf received by an antenna (not shown) is downconverted and demodulated, for generation of a video signal Sv, in accordance with a frequency band of a user selected broadcast channel.

15           The data extractor 103 extracts given data De included in the video signal Sv provided by the tuner 101.

          The data storage 107 stores the data De provided by the data extractor 103. The data storage 107 also outputs the stored data De to the data decoder 105 in response to a read request signal Sr to be provided therefrom.

20           The data decoder 105 decodes the data De provided by the data extractor 103 or the data storage 107, and generates decoded data Dd.

          The data presenter 109 is structured by a display device such as display, and presents the details of the decoded data Dd  
25           provided by the data decoder 105.

In such conventional data receiver Arc, however, it is uncertain when distribution data is updated. Therefore, whenever data comes, the data has to be written to the data storage 107. To be specific, even if the newly-provided data is identical to the one already in the data storage 107, the data already in the data storage 107 is overwritten with the data which is newly-provided but identical thereto.

When the data storage 107 is structured by a device being susceptible to deterioration from frequency of data writing thereto such as hard disk or nonvolatile memory, the same data is repeatedly written thereto more often than necessary to keep the data therein the latest. Thus, the life cycle of the data storage 107 gets shorter due to such unnecessary repeated writing.

In addition, as it is uncertain when the data is updated, the tuner 101 has to be left on an applicable channel.

Further, to keep desired data up-to-date, power has to always be supplied to all of the tuner 101, the data extractor 103, the data decoder 105, the data storage 107, and the data presenter 109 for bringing the data receiver Arc into operation.

A first object of the present invention is to provide a storage-type data receiver and a storage-type data reception method capable of cutting down, to a minimum, the frequency of data writing necessary to keep the storage data the latest, and a recording medium on which a storage-type data reception processing program is recorded.

A second object of the present invention is to provide a storage-type data receiver and a storage-type data reception method capable of keeping data in storage the latest while cutting down, to a minimum, the frequency of data writing without constantly setting the tuner to an applicable channel, and a recording medium on which a storage-type data reception processing program is recorded.

A third object of the present invention is to provide a storage-type data receiver and a storage-type data reception method capable of keeping data in storage the latest while cutting down, to a minimum, the frequency of data writing without constantly supplying power to constituents necessary to receive data, and a recording medium on which a storage-type data reception processing program is recorded.

#### DISCLOSURE OF THE INVENTION

The present invention has the following features to attain the objects above.

A first aspect of the present invention is directed to a storage-type data receiver of a type receiving data distributed from a data source and updated at irregular intervals, and next-update information indicating when the data is to be next updated, and storing the data, the receiver comprises:

a receiving device for receiving the data and next-update



information;

a storage for storing the data;

a data update detector for generating, after comparing  
current time and next-update time indicated by the next-update  
5 information, a data update time indication signal which indicates  
whether or not now is the time to update the data; and

a data storage controller for controlling storage of the  
data in the storage according to the data update indication  
signal.

10

As described in the foregoing, in the first aspect, storage  
of data can be done only when data in storage is updated.

According to a second aspect, in the first aspect, the  
15 storage is controlled to store the received data when the current  
time coincides with the next-update information.

As described in the foregoing, in the second aspect, storage  
of data is done only when data in storage is updated. Therefore,  
20 the data already in storage can be prevented from being  
overwritten with the same data.

According to a third aspect, in the first aspect, the  
receiving device comprises:

25 a tuner for arbitrarily selecting a broadcast channel

signal selected from a plurality of broadcast channel signals;  
and

a tuner controller for controlling channel tuning of the  
tuner according to the data update time indication signal.

5       According to a fourth aspect, in the third aspect, the tuner  
controller controls the tuner in such a manner as to tune itself  
with the arbitrarily selected channel when the current time  
coincides with the next-update information.

10       As described in the foregoing, in the fourth aspect, another  
data can be received when the data in storage is not overwritten  
with new data.

15       According to a fifth aspect, in the first aspect, further  
comprising a power supply controller for controlling power supply  
to the receiving device according to the data update indication  
signal.

20       According to a sixth aspect, in the fifth aspect, the power  
supply controller supplies power to the receiving device only when  
the current time coincides with the next-update information.

25       As described in the foregoing, in the sixth aspect, the  
power supply to the receiver is stopped when the data in storage  
is not overwritten with new data, thereby preventing unnecessary

power consumption.

According to a seventh aspect, in the fifth aspect, the  
power supply controller) supplies power to the data update  
5 detector regardless of the data update indication signal.

As described in the foregoing, in the seventh aspect, data  
stored can be overwritten with new data with a minimum power  
consumption.

10

According to an eighth aspect, in the third aspect, a  
storage data identification information device for generating  
identification information for specifying data to be stored,

according to the identification information, the tuner  
15 controller tunes the channel of the tuner to a broadcast channel  
through which the stored data is distributed.

As described in the foregoing, in the eighth aspect, data  
distributed from a plurality of data sources can be arbitrarily  
20 selected and stored.

According to a ninth aspect, in the eighth aspect, further  
comprising specified data extractor for extracting the specified  
data to be stored from the received data in accordance with the  
25 identification information.

A tenth aspect of the present invention is directed to a storage-type data reception method of receiving data distributed from a data source and updated at irregular intervals, and  
5 next-update information indicating when the data is to be next updated, and storing the data, the method comprises:

a reception step of receiving the data and next-update information;

a storage step of storing the data;

10 an update time determination step of determining whether or not now is the time to update the data after comparing current time and next-update time indicated by the next-update information; and

a data storage control step of effectuating the storage step  
15 according to the determination made in the update time determination step.

According to an eleventh aspect, in the tenth aspect, in the data storage control step, the storage step is effectuated  
20 only when the current time coincides with the next-update information.

According to a twelfth aspect, in the tenth aspect, the reception step further comprises:

25 a tuning step of arbitrarily selecting a broadcast channel

signal selected from a plurality of broadcast channel signals;  
and

a tuning control step of effectuating the tuning step only  
when the current time coincides with the next-update information.

5

According to a thirteenth aspect, in the tenth aspect, a  
computer program capable of activating a computer in such a manner  
that a device structured by the computer program and the computer  
can carry out the storage-type data reception method as claimed  
10 in claim 10.

According to a fourteenth aspect, in the tenth aspect, a  
computer program capable of causing a computer to carry out the  
storage-type data reception method as claimed in claim 10 when  
15 the product is run thereon.

According to a fifteenth aspect, in the tenth aspect, a  
computer program product stored on a medium readable by a computer,  
which comprises computer code means capable of carrying out the  
20 storage-type data reception method as claimed in claim 10 when  
the product is run thereon.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing the structure of a data  
25 receiver according to a first embodiment of the present invention.

FIG. 2 is a block diagram exemplarily showing the detailed structure of an in-data processing controller 40 shown in FIG. 1.

FIG. 3 is a flowchart showing the operation of the data receiver shown in FIG. 1 is operated.

FIG. 4 is a block diagram showing the structure of a data receiver according to a second embodiment of the present invention.

FIG. 5 is a flowchart showing the operation of the data receiver shown in FIG. 4 is operated.

FIG. 6 is a block diagram showing the structure of a data receiver according to a third embodiment of the present invention.

FIG. 7 is a flowchart showing the operation of the data receiver shown in FIG. 6 is operated.

FIG. 8 is a block diagram showing the structure of a data receiver according to a fourth embodiment of the present invention.

FIG. 9 is a flowchart showing the operation of the data receiver shown in FIG. 8 is operated.

FIG. 10 is a block diagram showing the structure of a conventional data receiver.

#### BEST MODE FOR CARRYING OUT THE INVENTION

To describe the present invention in detail, the description is made with reference to the accompanying drawings.

Next below, a storage-type data receiver according to a first embodiment of the present invention is described in detail with reference to FIG. 1, FIG. 2, and FIG. 3, a storage-type data receiver according to a second embodiment with reference to FIG. 4 and FIG. 5, a storage-type data receiver according to a third embodiment with reference to FIG. 6 and FIG. 7, and a storage-type data receiver according to a fourth embodiment with reference to FIG. 8 and FIG. 9.

(First Embodiment)

First, by referring to FIG. 1, the structure of the storage-type data receiver according to the first embodiment of the present invention is described. Herein, this embodiment is suitable for an environment where data to be stored is distributed from a single data source at a given time.

A storage-type data receiver Arp1 of this embodiment is provided with a tuner 1, a data extractor 3, a date decoder 5, a next-update information extractor 7, a next-update information recorder 9, a comparator 11, a storage controller 13, a clock 15, a data storage 17, a data presenter 19, and a power supply 21.

In the tuner 1, a broadcast wave Srf received by an antenna (not shown) is demodulated, for generation of a video signal Sv, in accordance with a frequency band of a user selected broadcast channel.

The data extractor 3 extracts data De included in the video signal Sv provided by the tuner 1.

The data decoder 5 decodes (interprets) the data De provided by the data extractor 3 so as to output decoded data Dd.

The data presenter 19 is exemplarily structured by an indicator such as display device or a printing device such as printer, and presents the details of the decoded data Dd provided by the data decoder 5.

The next-update information extractor 7 extracts and outputs, from the decoded data Dd provided by the data decoder 5, next-update information Inu indicating when the decoded data Dd is to be updated next.

The next-update information recorder 9 records the next-update information Inu provided by the next-update information extractor 7.

The clock 15 outputs current date/time information Ict indicating the current date/time.

The comparator 11 compares the current date/time information Ict provided by the clock 15 with the next-update information Inu recorded in the next-update information recorder 9, and outputs a determination signal Sj indicating the comparison result.

The data storage 17 is exemplarily structured by a recording medium such as hard disk or nonvolatile memory, and stores the decoded data Dd provided by the data decoder 5.

The storage controller 13 generates, in accordance with the determination signal Sj provided by the comparator 11, a storage



control signal Sc which controls the data storage and read operation of the data storage 17, and the signal Sc therefrom. In detail, in accordance with the storage control signal Sc, the storage controller 13 stores the decoded data Dd provided by the data decoder 5, and also reads the stored decoded data Dd out to the data presenter 19.

The power supply 21 is a device supplying power to each constituent of the storage-type data receiver Arpl. The user turns on/off the power supply 21 so as to start/stop the storage-type data receiver Arpl.

In this embodiment, the tuner 1, the data extractor 5, and the data decoder 5 construct the source of data to be stored;

the next-update information extractor 7, the clock 15, and the comparator 11 are for detecting the update timing of the data to be stored; and

the storage controller 13 and the data storage 17 are for storing and reading the data.

In this embodiment, the data decoder 5, the next-update information extractor 7, the next-update information recorder 9, the comparator 11, and the storage controller 13 are all the constituents of an in-data processing controller 40. The in-data processing controller 40 is preferably realized by software.

FIG. 2 shows the exemplary detailed hardware structure of the aforementioned in-data processing controller 40. The in-data processing controller 40 is provided with a CPU 40A, a ROM

40B, and a RAM 40C. The ROM 40B contains a data reception processing program which controls the comprehensive operation of the in-data processing controller 40. The CPU 40A runs the data processing program contained in the ROM 40B on the RAM 40C.

5 Thereby the data decoder 5, the next-update information extractor 7, the next-update information recorder 9, the comparator 11, and the storage controller 13, all of which are the constituents of the in-data processing controller 40, are each put to function.

To be specific, the CPU 40A runs the data reception  
10 processing program contained in the ROM 40B according to the data De and the current date/time information Ict respectively provided by the data extractor 3 and the clock 15, and generates the decoded data Dd and the storage control signal Sc.

Next, by referring to the flowchart shown in FIG. 3, it is  
15 described how the aforementioned in-data processing controller 40 in the storage-type data receiver Arp1 is operated.

Once the storage-type data receiver Arp1 is started, first  
in the tuner 1, a broadcast signal in a user predesignated channel frequency band is selected out of the broadcast wave Srf received  
20 by the antenna, and then the video signal Sv included therein is outputted to the data extractor 3. The data extractor 3 extracts the data De from the video signal Sv. The data decoder 5 decodes the data De to generate the decoded data Dd. The data decoder 5 outputs the generated decoded data Dd to both the next-update  
25 information extractor 7 and the data presenter 19. The data

presenter 19 presents the details of the decoded data Dd.

On the other hand, the following processing is carried out on the basis of the decoded data Dd provided to the next-update information extractor 7.

5 First, in step S10, the storage controller 13 generates the storage control signal Sc, and then causes the data storage 17 to store the decoded data Dd provided by the data decoder 5. Thereafter, the procedure goes to the next step S20.

10 In step S20, the next-update information extractor 7 extracts the next-update information Inu from the decoded data Dd provided by the data decoder 5. The extracted next-update information Inu is recorded in the next-update information recorder 9. Thereafter, the procedure goes to the next step S30.

15 In step S30, the comparator 11 reads the next-update information Inu recorded in the next-update information recorder 9. Thereafter, the procedure goes to the next step S40.

In step S40, the comparator 11 reads the current date/time information Ict from the clock 15. Thereafter, the procedure goes to the next step S50.

20 In step S50, it is determined whether or not the current time T (ict) indicated by the current date/time information Ict read in step S40 and the update time T (inu) indicated by the next-update information Inu read in step S30 coincide with each other.

25 When the current time T (ict) does not coincide with the

next-update time (inu), the determination is No, and then the processing in this step is repeated. Then, when the current time T (ict) coincides with the next-update time (inu), the determination becomes Yes and the procedure goes to the next step

5 S60.

In step S60, the storage controller 13 generates the storage control signal Sc, and controls the data storage 17 so as to store the decoded data Dd provided by the data decoder 5. In detail, the decoded data Dd already recorded in the data storage 17 is

10 updated by another decoded data Dd newly-provided by the data decoder 5 at the next-update time T (inu). To confirm the storage completion of the decoded data Dd, that is, recording completion to the data storage 17, for example, end of the decoded data Dd is referred to. The procedure then goes to the next step S70.

15 In step S70, the next-update information Inu extracted by the next-update information extractor 7 from the decoded data Dd is stored in the next-update information recorder 9. To be specific, the next-update information Inu already recorded in the next-update information recorder 9 is updated, as is the

20 next-update time T (inu), by the next-update information Inu currently newly-provided by the next-update information extractor 7. Note that, the newly-provided next-update information Inu stored in this step indicates when the decoded data Dd recorded in step S60 is to be updated next.

25 The procedure then returns to step S30. Thereafter, steps

S30, S40, S50, S60, and S70 are repeated so that the decoded data Dd stored in the data storage 17 is updated by the newly-provided decoded data Dd for every next-update time T (inu), which is the time when the decoded data Dd is updated by the next-update information Inu.

Herein, although in step S60, the decoded data Dd already stored in the data storage 17 is updated by the newly-provided decoded data Dd, the newly-provided decoded data Dd may be stored, alternatively, together with the already-stored decoded data Dd. Similarly in step S70, instead of updating the already-stored next-updated information Inu by the newly-provided next-updated information Inu, the newly-provided next-updated information Inu may be stored together with the already-stored next-updated information Inu.

As is described in the foregoing, according to the storage-type data receiver Arp1 of this embodiment, only when the decoded data Dd provided by a single data source is updated, the decoded data Dd already stored in the data storage 17 is updated by the newly-provided decoded data Dd. In this manner, the frequency of data writing to the data storage 17 can be cut down to a minimum. To be specific, the data can remain the latest while the deterioration of the data storage 17 being suppressed.

(Second Embodiment)

The structure of the data receiver according to the second embodiment of the present invention is described by referring to

FIG. 2. Herein, this embodiment is suitable for an environment where data to be stored is distributed from one of a plurality of data sources at a given time.

In a storage-type data receiver Arp2 of this embodiment, a tuner controller 23 is additionally provided so as to connect the comparator 11 and the tuner 1 found in the storage-type data receiver Arp1 shown in FIG. 1. Further, a data reception processing program contained in the ROM 40B is slightly different from the one in the storage-type data receiver Arp1.

Based on the judgement signal Sj provided by the comparator, the tuner controller 23 provides the tuner 1 with a tuning channel signal St indicating a frequency band of a broadcast channel to tune among the received broadcast wave Srf. The tuner 1 outputs, in accordance with the tuning channel signal St, the video signal Sv obtained from the broadcast wave Srf of the predetermined channel to the data extractor 3.

Next, by referring to the flowchart shown in FIG. 5, it is described how the storage-type data receiver Arp2 of this embodiment is operated to receive and process data. Note that, the flowchart of this embodiment is additionally provided with step S4 before step S20 found in the flowchart of the first embodiment shown in FIG. 3, and step S54 between steps S50 and S60 thereof. Hereinafter, these additionally-provided steps S4 and S54 are more focused in the operational description next below.

Once the storage-type data receiver Arp2 is started, first of all,

in step S4, the user designates a desired channel for reception to the tuner controller 23. The tuner controller 23 records the user designated channel and also generates, for output to the tuner 1, the tuning channel signal St which indicates that the channel.

The tuner 1 tunes itself, in accordance with the tuning channel signal St, with the broadcast wave on the user designated channel. As a result, from the broadcast wave Srf received by the antenna, the tuner 1 obtains the video signal Sv being broadcast on the user's desired channel and outputs it to the data extractor 3.

Thereafter, processing in steps S10, S20, S30, S40, and S50 is executed right after the aforementioned storage-type data receiver Arp1 is started. In step S50, the comparator 11 outputs the determination signal Sj to both the storage controller 13 and the tuner controller 23 when the determination is Yes, meaning that, the current time reaches the next-update time T(inu). Then, the procedure goes to the next step S54.

In step S54, a tuner control part 12 outputs the tuning channel signal St to the tuner 1 in response to the determination signal Sj. The tuner 1 is controlled by the tuning channel signal St in such a manner as to tune itself with the channel designated by the user in step S4. As a result, the decoded data Dd included

in the video signal Sv distributed on the user desired broadcast channel is provided by the data decoder 5. Thereafter, steps S60, S70, S30, S40, and S50 in the foregoing are repeated to be ready for the next update of the data De.

5 As described in the foregoing, according to the storage-type data receiver Arp2 of this embodiment, only when the data distributed from one arbitrary data source out of the plural is updated, the tuner 1 is tuned with a broadcast channel corresponding to that arbitrary data source, and thus the decoded  
10 data Dd stored in the data storage 17 is updated to be the latest.

Consequently, there is no need to leave the tuner 1 on a channel continuously for data broadcasting, and also the frequency of writing to the data storage 17 can be cut down to a minimum. In this manner, the tuner 1 can be used for another  
15 data source except during the decoded data Dd being updated, and data can remain the latest with deterioration of the data storage 17 being suppressed.

(Third Embodiment)

By referring to FIG. 6, the structure of the data receiver  
20 according to the third embodiment of the present invention is described. Herein, this embodiment is suitable, in an environment where data to be stored is distributed from one of plurally-provided data sources at a given time, for updating data in storage with a minimum power consumption.

25 A storage-type data receiver Arp3 of this embodiment is



additionally provided with a power supply controller 25 between the comparator 11 and the power supply 21 found in the storage-type data receiver Arp2 shown in FIG. 4 to connect. Further, a data reception processing program contained in the ROM 40B is slightly different from the one in the storage-data receiver Arp3.

The power supply controller 25 generates a power control signal Sp which controls power supply from the power supply 21 to each constituent of the storage-type data receiver Arp3 in response to the determination signal Sj provided by the comparator 11. The power supply 21 varies its power supply to the constituents of the data receiver Arp3 according to the power supply control signal Sp.

Next, by referring to the flowchart in FIG. 7, it is described how the storage-type data receiver Arp3 of this embodiment is operated to receive and process data. Herein, the flowchart of this embodiment is additionally provided with step S22 before steps S20 and S30 found in the flowchart of the second embodiment shown in FIG. 5, and step S52 between steps S50 and S54 thereof. Note that, the procedure after step S70 goes not to step S30 but to newly-provided step S22. These additionally-provided steps S22 and S52 are more focused in the operational description next below.

Once the storage-type data receiver Arp3 is started, processing in steps S4, S10, and S20 in the aforementioned second embodiment is first carried out. To be specific, the decoded data

Dd distributed on a user desired broadcast channel is stored, and the next-update information Inu which indicates when the decoded data Dd is to be updated next is also stored, and then the procedure goes to the next step S22.

5           In step S22, in response to the determination signal Sj provided by the comparator 11, the power supply controller 25 stops the power supply for any constituent of the storage-type data receiver Arp3 of this embodiment which is not necessary for next update of the decoded data Dd. In this example, the power  
10 supply to the constituents in charge of display typically including the tuner 1, the data extractor 3, and the tuner controller 23 is stopped. On the other hand, the power supply to the constituents in charge of control typically including the comparator 11, the clock 15, and the power supply controller 25  
15 is kept on.

          Thereafter, the processing in steps S30, S40, and S50 is carried out by the control constituents. Then, in step S50, when the determination is Yes, that is, when the current time reaches or coincides with the next-update time T (inu), the procedure goes  
20 to the next step S52.

          In step S52, the power supply for the display constituents once stopped in step S22 is started again. Thereafter, processing in steps S54, S60, and S70 being handled by the display constituents is carried out. Then the procedure again returns  
25 to step S22, and the power supply for the display constituents

is stopped.

In this manner, steps S22, S30, S40, S50, S52, S54, S60, and S70 are repeated to be ready for the update of the decoded data Dd.

5 As is described in the foregoing, according to the storage-type data receiver Arp3 of this embodiment, only when the data distributed from one arbitrary data source out of the plural is updated, the power is supplied to the distribution data source and the data storage part, and the tuner 1 is tuned to a channel  
10 corresponding to the desired data source, whereby the decoded data Dd is stored in the data storage 17.

As a result, the frequency of writing to the data storage 17 can be cut down to a minimum without constant power supply to any constituent necessary to receive data, and without  
15 continuously setting the tuner 1 to an channel to store data.

Accordingly, the power consumption is reduced, the tuner 1 can be used for another data source except during the data being updated, and the data can remain the latest with the deterioration of the data storage 17 being suppressed.

20 (Fourth Embodiment)

By referring to FIG. 8, the structure of the data receiver according to the fourth embodiment of the present invention is described. Note that, this embodiment is suitable for, in an environment where data to be stored is each distributed from a  
25 plurality of data sources at a given time, updating data in storage

with a minimum power consumption.

A storage-type data receiver Arp4 of this embodiment is additionally provided with a storage program extractor 27 and a storage program register 29 in the storage-type data receiver Arp3 shown in FIG. 6. Further, a data reception processing program contained in the ROM 40B is slightly different from the one in the storage-type data receiver Arp1.

The storage program extractor 27 connects the data decoder 5 to the next-update information extractor 7, and the storage program register 29 is connected to both the storage program extractor 27 and the tuner controller 23. Consequently, in this embodiment, an in-data processing controller 40R is structured by the storage program extractor 27 which is additionally provided to the aforementioned in-data processing controller 40.

The user operates the storage program register 29 so that program identification information Ip can be registered in the storage program extractor 27. Note that the program identification information is formed by various parameters each indicating broadcast programs whose decoded data Dd is favorably required to be stored. The program identification information Ip, typical of EPG, includes information such as program broadcast channel, program identification number, program start time and end time, and program details, and with other information, can be additionally provided as required. Further, the storage program register 29 outputs, to the tuner controller 23, broadcast

channel information included in the program identification information Ip as the tuning channel signal St.

Next, by referring to the flowchart shown in FIG. 9, it is described how the storage-type data receiver Arp4 of this embodiment is operated to receive and process data. Note that, the flowchart of this embodiment is additionally provided with step S2 before step S4 in the flowchart of the third embodiment shown in FIG. 7, step S6 between steps S4 and S10, and step S56 between steps S54 and S60. Further, step S4 is replaced with step S4R. These additionally-provided steps S2, S4R, S6, and S56 are more focused in the operational description next below.

Once the storage-type data receiver Arp4 is started, first of all,

in step S2, the user the user operates the storage program register 29 so that the program identification information Ip indicating the broadcast programs whose decoded data Dd favorably required to be stored is registered in the storage program extractor 27. The procedure then goes to the next step S4.

In step S4R, the program identification information Ip registered in step S2, the tuner controller 23 outputs the tuning channel signal St which indicates the user designated channel to the tuner 1. Then, the procedure goes to the next step S6.

In step S6, it is determined whether or not the currently distributed program is the program designated in step S2. Herein, the details of the currently distributed program can be referred

to the decoded data Dd provided by the date decoder 5. The determination becomes Yes when the desired program is distributed, and then the procedure goes to the next step S10.

After the processing in steps S10, S20, S22, S30, S40, S50, S52, and S54 is carried out, the procedure goes to the next step S56. In detail, the decoded data Dd on the user desired channel is stored (S60) and also the next-update information Inu is stored (S70), then the power supply to the display constituents is stopped (S22). Thereafter, when the current time reaches the next-update time T (inu)(S50), the power supply to the display constituents is started again (S52) to be ready for storage of the latest distribution data of any desired program.

In step S56, the storage program extractor 27 determines, in a similar manner to the processing in step S6, whether or not the currently distributed program is the program designated in step S2. When the determination becomes Yes when the desired program is distributed, and the procedure goes to the next step S60. Thereafter, steps S60, S70, S22, S30, S40, and S50 is repeated to be ready for the next update of the data De.

As is described in the foregoing, according to the storage-type data receiver Arp4 of this embodiment, the power supply necessary to receive data is turned on only when to-be-stored data each distributed from a plurality of data sources at a given time is updated, and the tuner 1 is tuned to the corresponding channel, and then the data De is written to the data

storage 17. As a result, the frequency of writing to the data storage 17 can be cut down to a minimum without continuous power supply to any constituent necessary to receive data, and without setting the tuner 1 to the corresponding channel to store data.

5           Accordingly, the power consumption is reduced, the tuner 1 can be used for another data source except during the data being updated, and further the data can remain the latest with the deterioration of the data storage 17 being suppressed.

10           Note that, in this embodiment, the recording medium for recording the data reception processing program is the ROM 40B. However, other semiconductor memory may be used as an alternative to the ROM 40B, or other recording medium such as floppy disk, hard disk, and CD-ROM will do. The recorded data reception processing program can be read by a reading device suitable for  
15           such recording medium, needless to say.

          In the above-described embodiment, the data decoder 5, the next-update information extractor 7, the next-update information recorder 9, the comparator 11, and the storage controller 13 are structured to be implemented via software, the data decoder 5,  
20           the next-update information extractor 7, the next-update information recorder 9, the comparator 11, and the storage controller 13 may be partially or wholly structured to be implemented via hardware.

          In the above-described embodiment, although the  
25           description is made for a case where the present invention is

applied to the data receiver and the data reception method of a  
type receiving the broadcast-type service, the present invention  
can be applied to the data receiver, e.g., personal computer, and  
the data reception method of a type receiving the  
5 communication-type service such as the Internet.

#### INDUSTRIAL APPLICABILITY

As is obvious from the above, the present invention can be  
applied to storage-type data reception in a data receiver which  
10 receives broad-cast type service, or a data receiver such as  
personal computer which receives communication-type service such  
as the Internet.



WHAT IS CLAIMED IS:

1. A storage-type data receiver for receiving data being updated at irregular intervals and next-update information indicating when said data will be next updated, both distributed by a data source to store said data therein, said receiver

5 comprising:

reception means for receiving said data and next-update information;

storage means for storing said data;

10 data update detection means for comparing current time and next-update time indicated by said next-update information to generate a data update time indication signal indicating whether it is the time to update said data, or not; and

15 data storage control means for controlling storage of said data in said storage means based on said data update indication signal.

2. A storage-type data receiver as claimed in claim 1, wherein said data storage control means controls said storage means to store said received data when the current time coincides with said next-update information.

3. A storage-type data receiver as claimed in claim 1, wherein said reception means further comprises:

tuner means for arbitrarily selecting a signal of broadcast channel among plural signals of broadcast channels; and

5 tuner control means for controlling channel selection by said tuner means based on said data update time indication signal.

4. A storage-type data receiver as claimed in claim 3, wherein said tuner control means controls said tuner means in such a manner as to tune itself with said arbitrarily selected channel when the current time coincides with said next-update information.

5. A storage-type data receiver as claimed in claim 1, further comprising power supply control means for controlling power supply to said reception means based on said said data update indication signal.

6. A storage-type data receiver as claimed in claim 5, wherein said power supply control means makes power supplied to said reception means only when the current time coincides with said next-update information.

7. A storage-type data receiver as claimed in claim 5, wherein said power supply control means makes power supplied to said data update detection means regardless of said said data update indication signal.



step based on the determination made in said update time  
15 determination step.

11. A storage-type data reception method as claimed in claim  
10, wherein, in said data storage control step, said storage step  
is effectuated only when the current time coincides with said  
next-update information.

12. A storage-type data reception method as claimed in claim  
10, wherein said reception step further comprises:

a tuning step of arbitrarily selecting a signal of  
broadcast channel among plural signals of broadcast channels; and

5 a tuning control step of effectuating said tuning  
step only when the current time coincides with said next-update  
information.

13. A computer program capable of activating a computer in such  
a manner that a device structured by the computer program and the  
computer can carry out the storage-type data reception method as  
claimed in claim 10.

14. A computer program capable of causing a computer to carry  
out the storage-type data reception method as claimed in claim  
10 when the product is run thereon.

15. A computer program product stored on a medium readable by a computer, which comprises computer code means capable of carrying out the storage-type data reception method as claimed in claim 10 when the product is run thereon.

## ABSTRACT OF THE DISCLOSURE

A storage-type data receiver (Arp1) for receiving data (Dd) being updated at irregular intervals and next-update information (Inu) indicating when the data (Dd) is to be updated next (T(Inu)), both distributed by a specific data source (1,3,5) to store the data therein, the receiver comprising: a receiver (1,3,5) receiving the distributed data (Dd) and the next-update information (Inu);

a storage (13) storing the received data (Dd); a next-update information recorder (9) recording the received next-update information (Inu); a clock (15) indicating the current time (T(Ict)), a data update detector (11) generating a data update indication signal (Sj) which indicates whether it is the time to update the data (Dd) by comparing the current time (T(Ict)) and the next-update time (T(Inu)); and a data storage controller (17) controlling the storage of the received data (Dd) in the storage (13) based on the data update indication signal (Sj).

Fig. 1

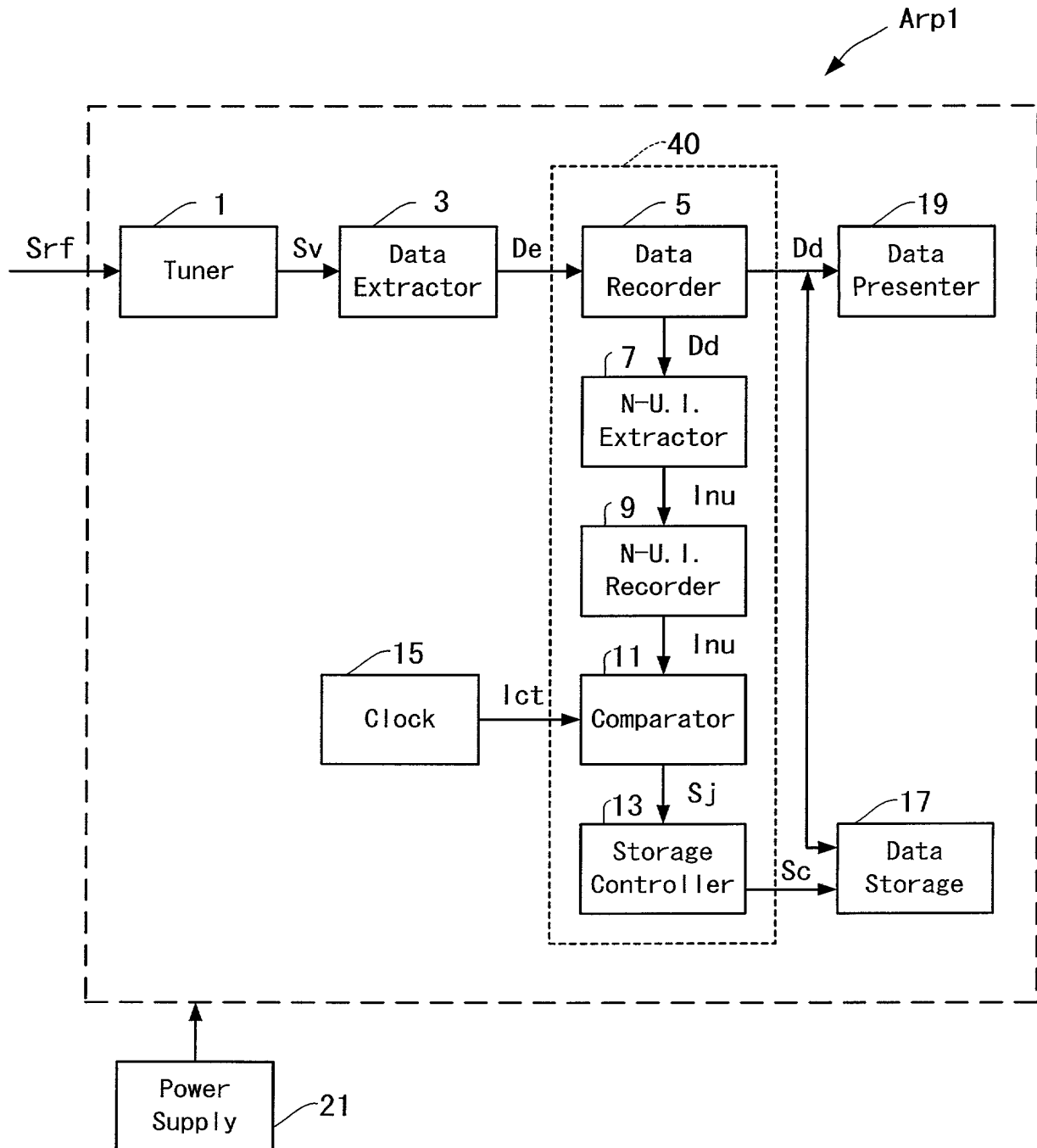


Fig. 2

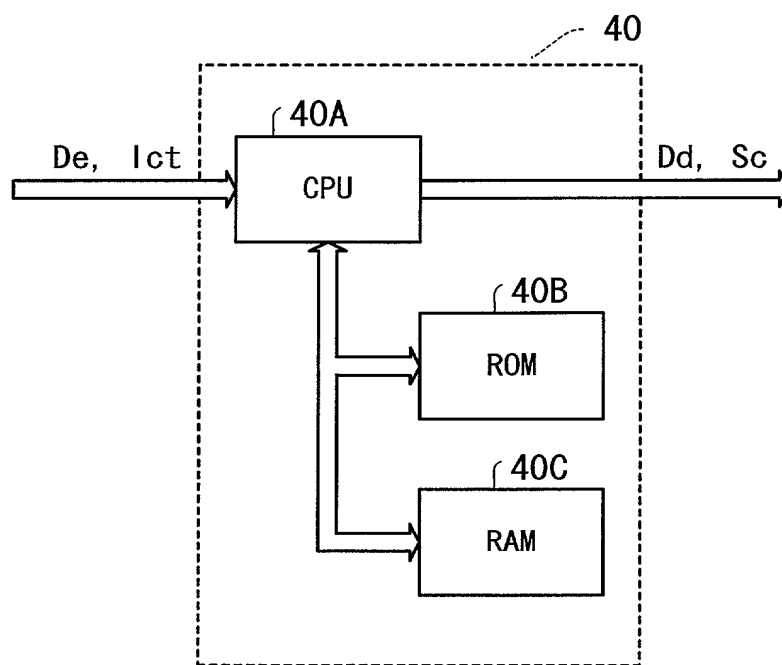




Fig. 3

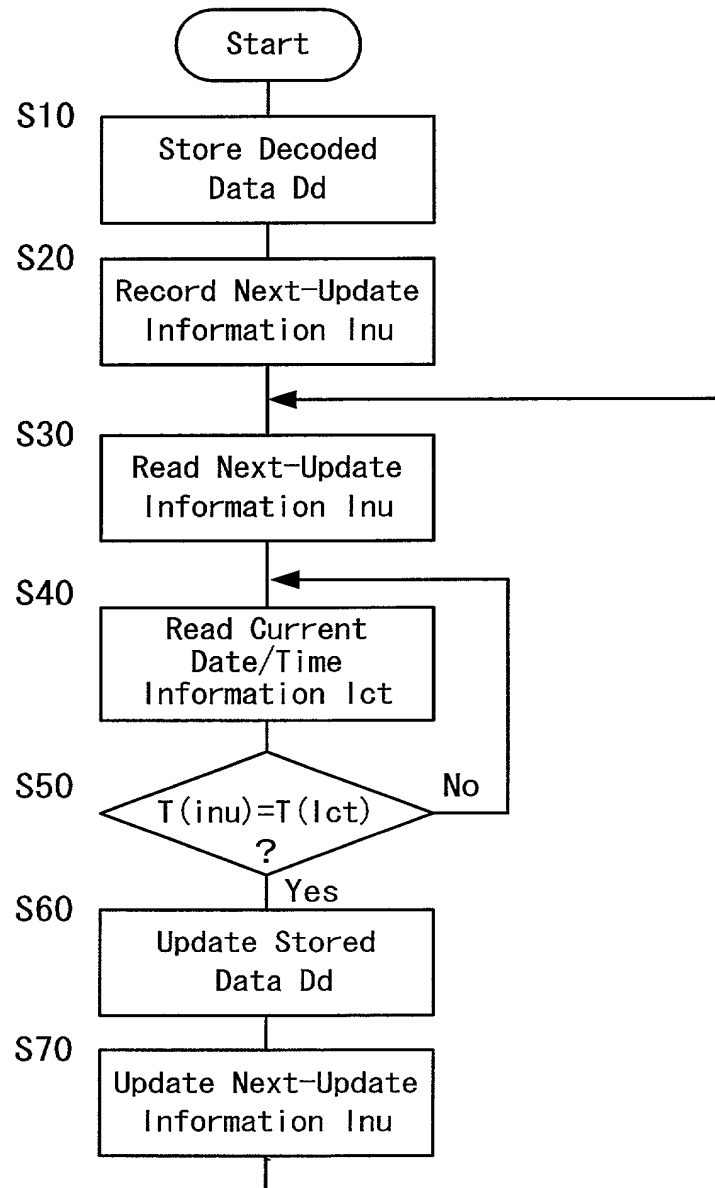


Fig. 4

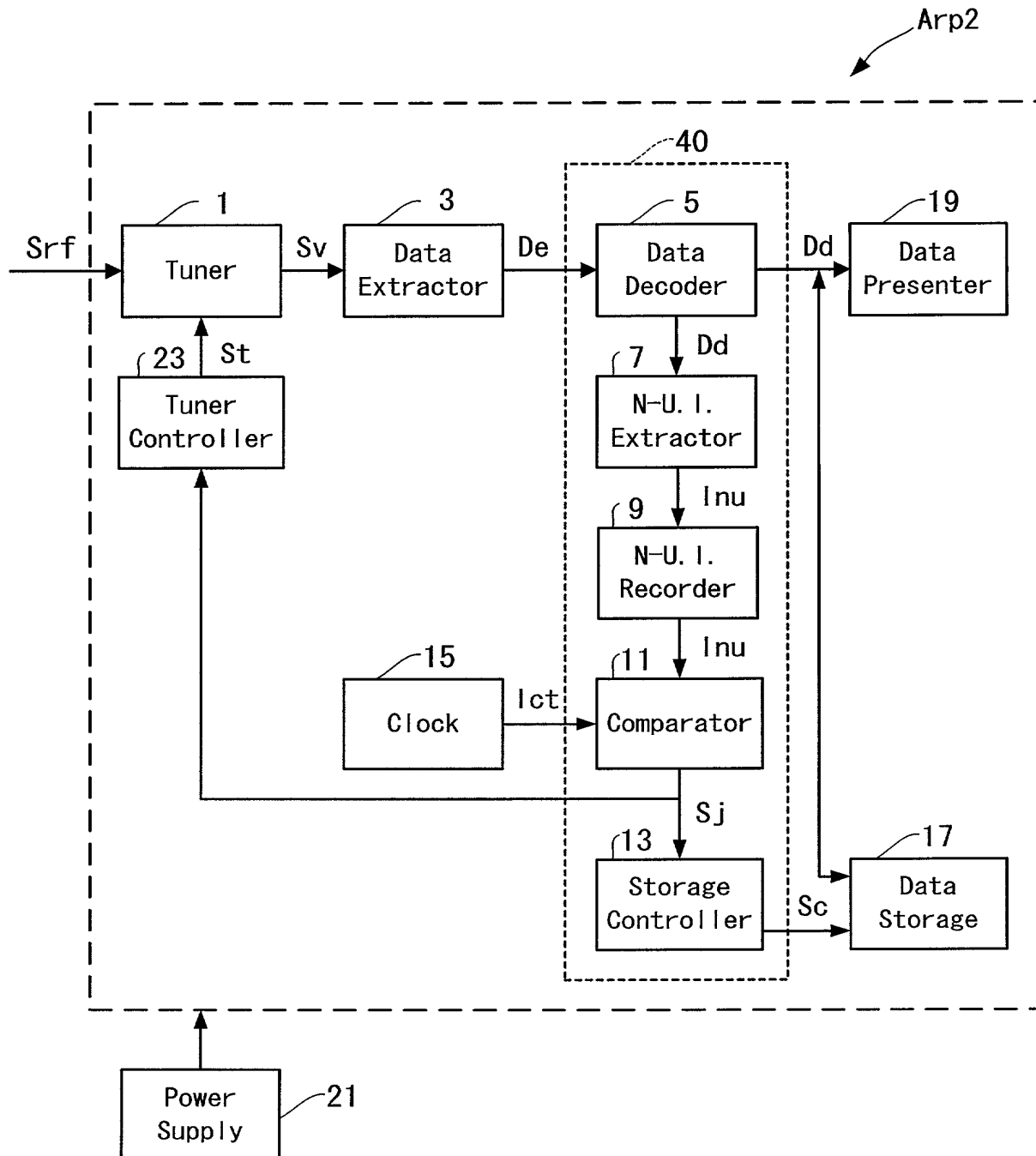


Fig. 5

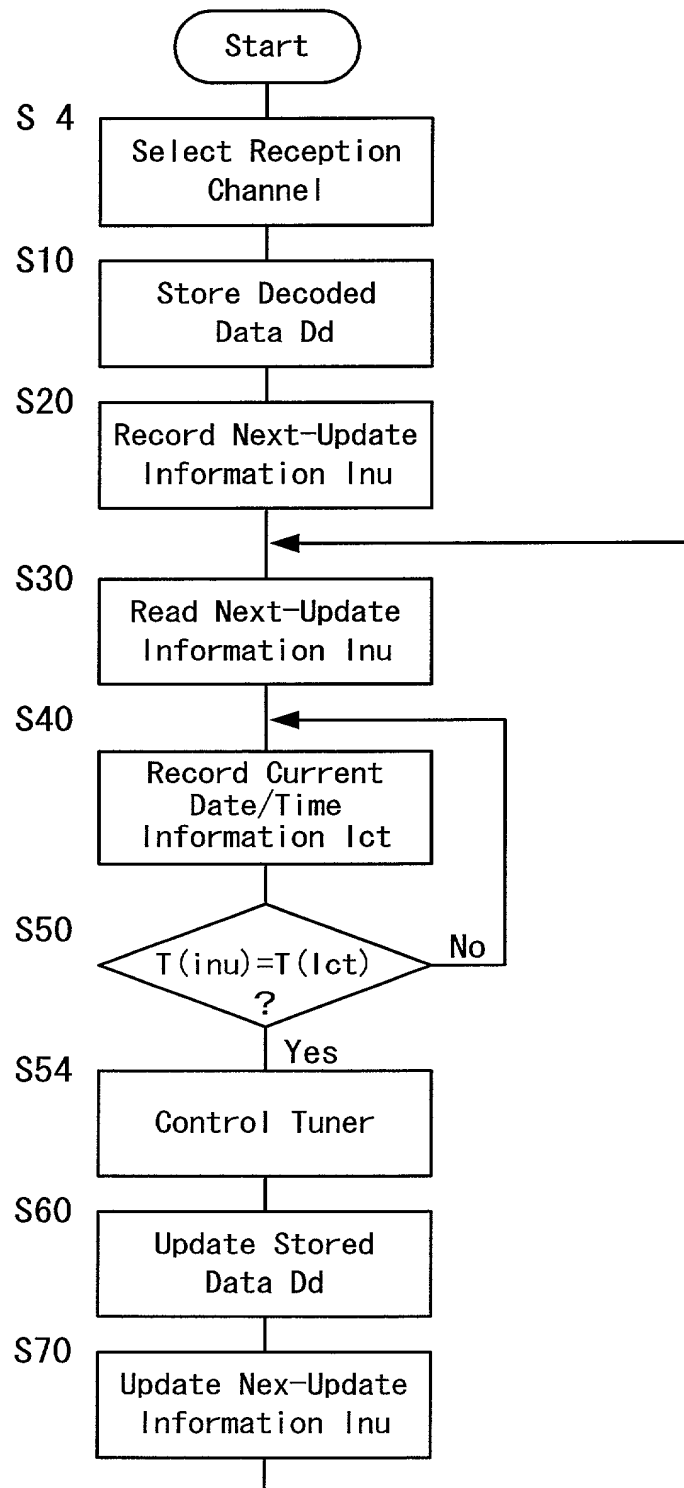


Fig. 6

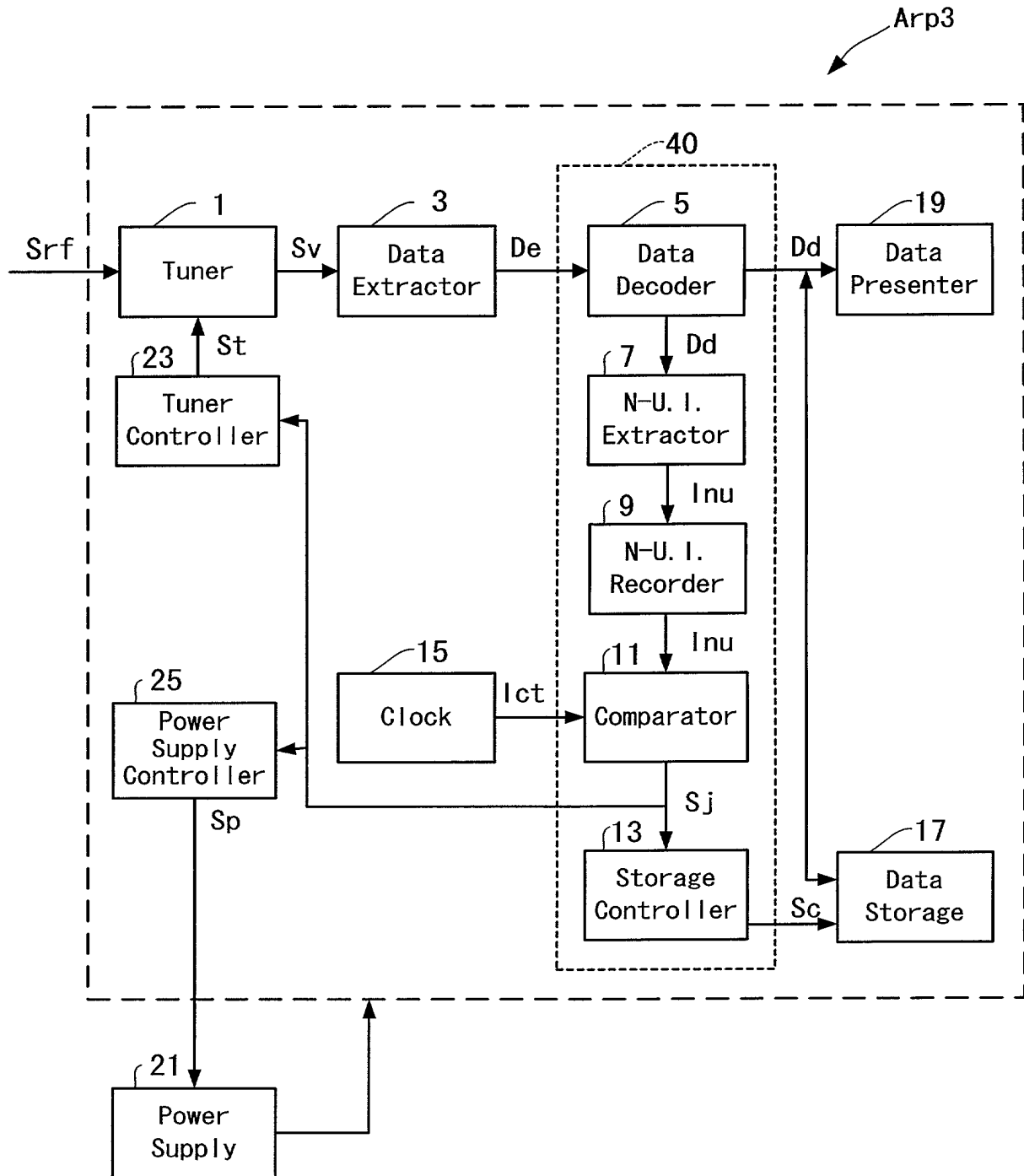


Fig. 7

09/530396

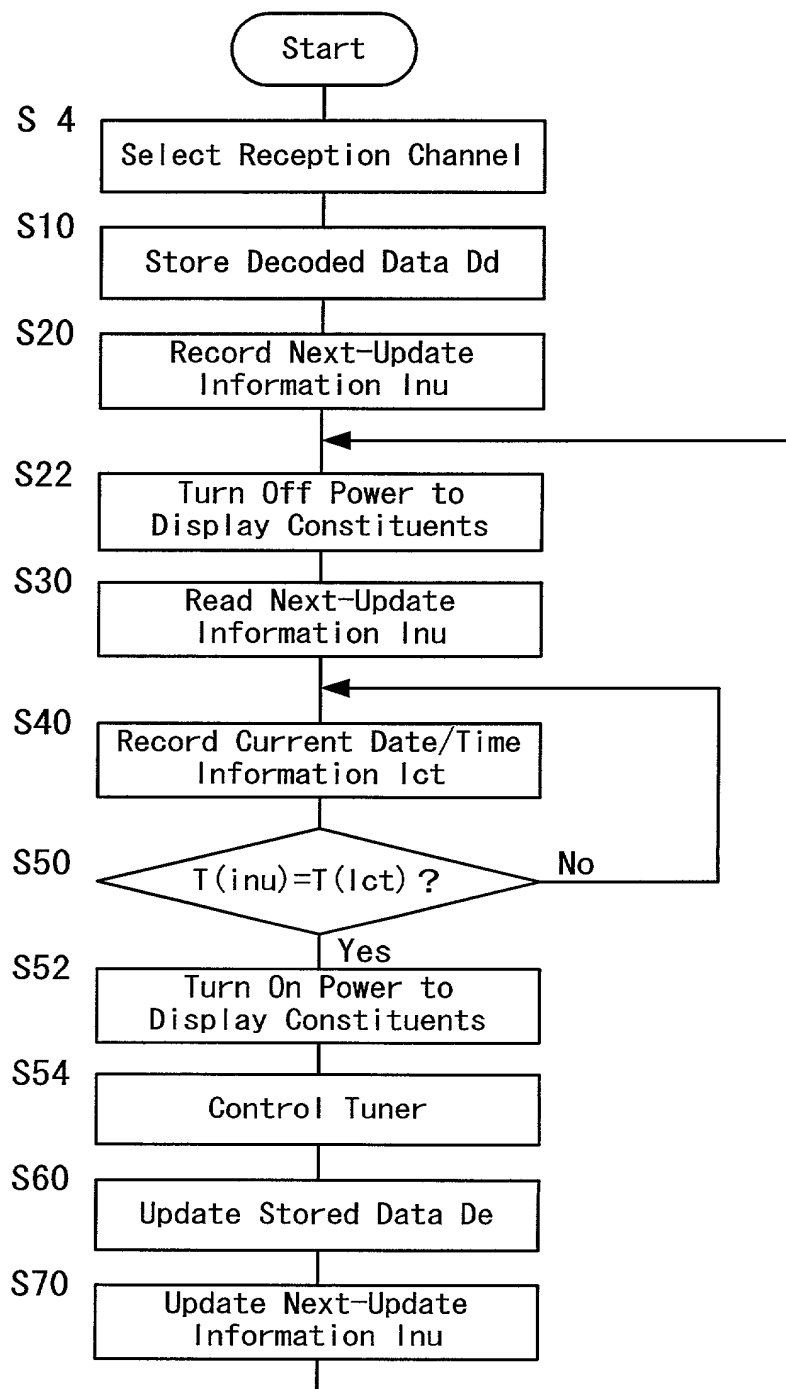


Fig. 8

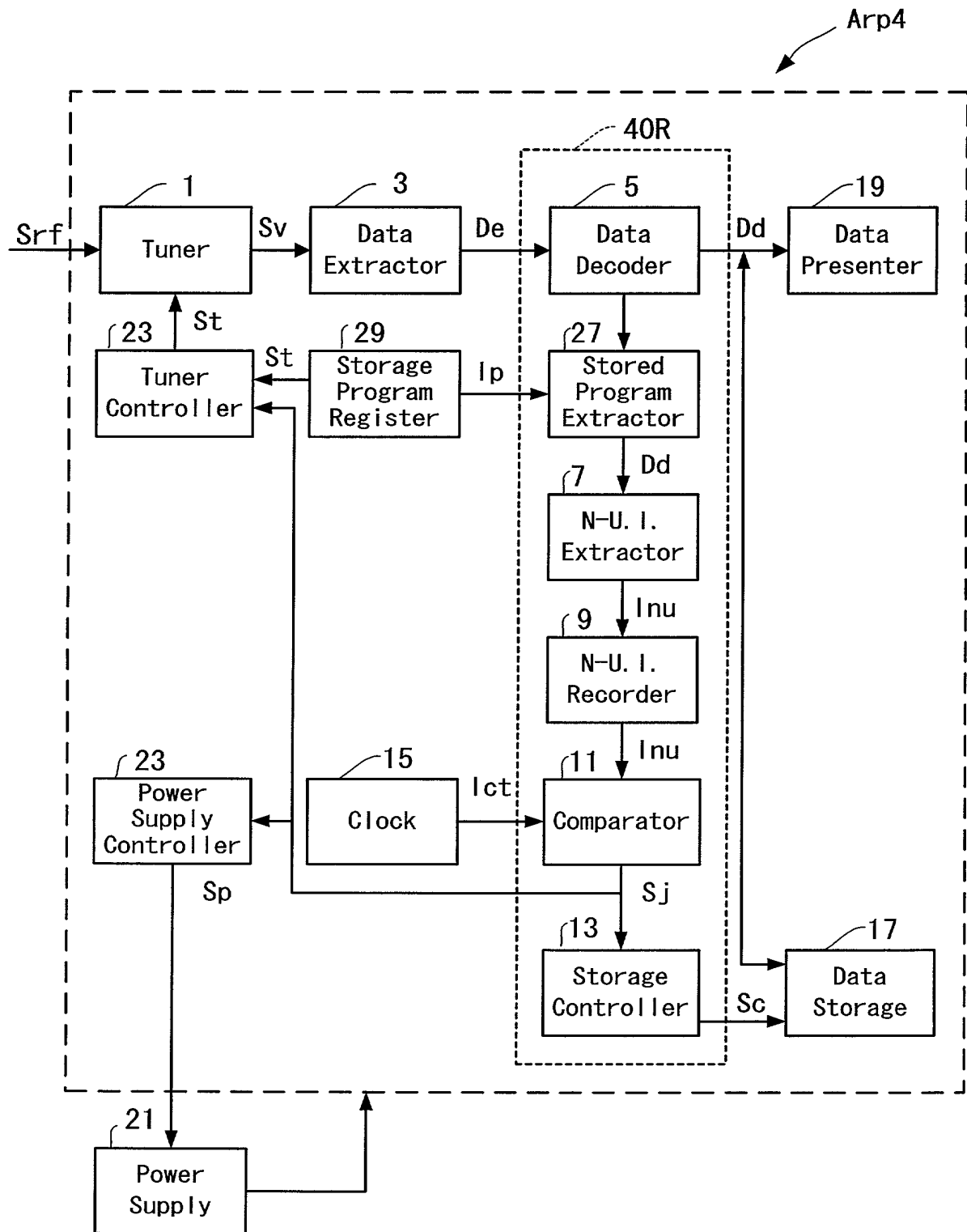


Fig. 9

09/530396

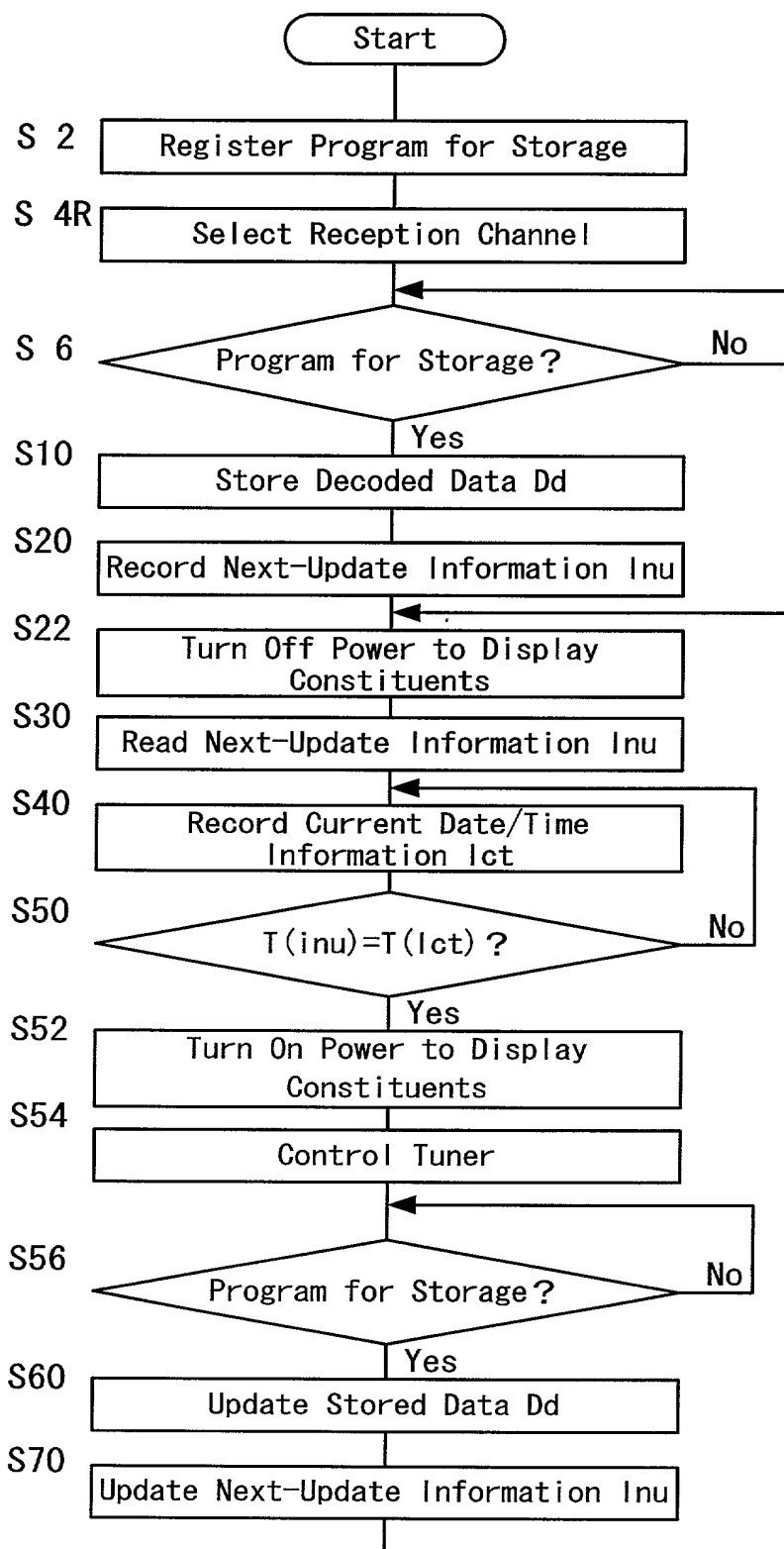
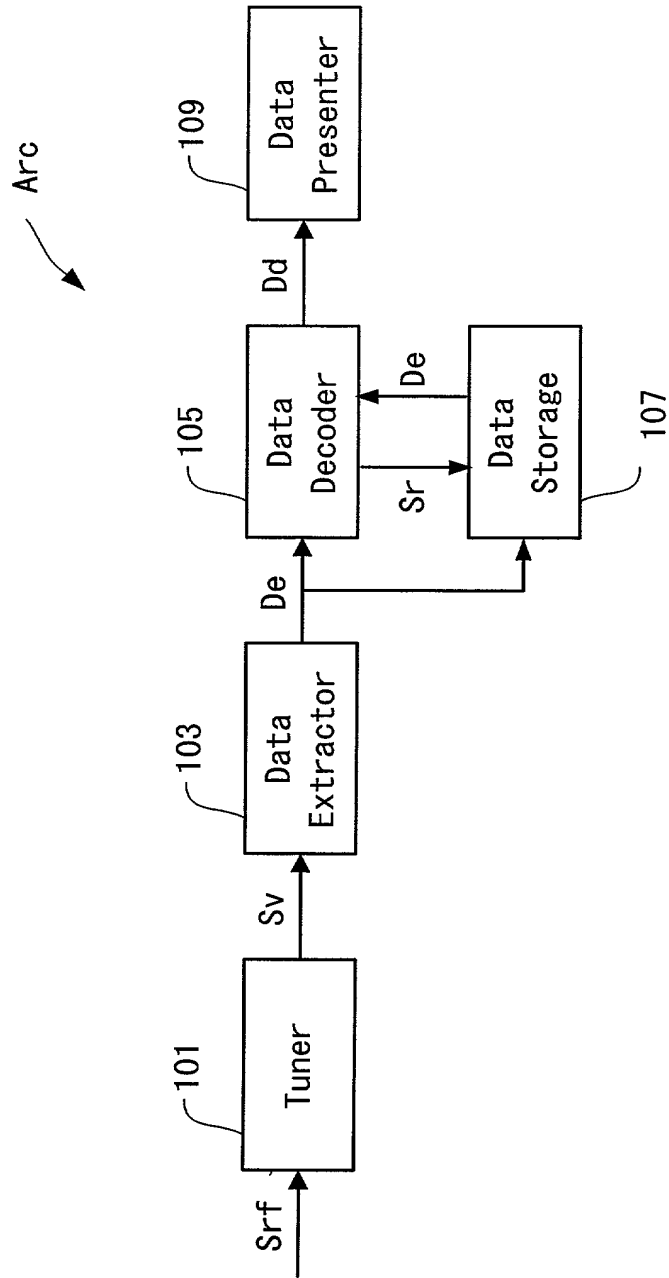


Fig. 10





**DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION**

( ) Original ( ) Supplemental ( ) Substitute (X) PCT ( ) Design

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Title: STORAGE-TYPE DATA RECEIVER

of which is described and claimed in:

- ( ) the attached specification, or  
 (X) the specification in the application Serial No. PCT/JP99/04510 filed August 23, 1999 ;  
 and with amendments through \_\_\_\_\_ (if applicable), or  
 ( ) the specification in International Application No. PCT/ \_\_\_\_\_, filed \_\_\_\_\_, and as amended  
 on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge my duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim priority benefits under Title 35, United States Code, §119 (and §172 if this application is for a Design) of any application(s) for patent or inventor's certificate listed below and have also identified below any application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED
Japan	244864/1998	August 31, 1998	Yes

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS: PATENTED, PENDING, ABANDONED

And I hereby appoint John T. Miller, Reg. No. 21,120; Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Jeffrey Noltan, Reg. No. 25,408; Warren M. Cheek, Jr., Reg. No. 33,367; Nils E. Pedersen, Reg. No. 33,145 and Charles R. Watts, Reg. No. 33,142, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., attorneys to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys named herein to accept and follow instructions from Ogasawara Patent Office as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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